

Session: TH3B

Sources

Chair

M. Odyniec

Hewlett-Packard

Co-Chair

A.P.S. Khanna

Hewlett-Packard

Rapid growth of wireless communication systems presents new challenges to oscillator design. These include integration and miniaturization of the circuit, design with low DC voltage and low power consumption. At the same time we still want to get low phase noise and high output power.

In this session we will have three papers on fully integrated VCO's that address miniaturization and low power consumption. One paper is on new methods of power combining. Finally, two papers on low-noise DRO's and injection locking will be presented.

TH
3B

1:40 pm - 3:30 pm Thursday, June 20, 1996
Room 132

